BIBLIOGRAPHY OF 1989 CRC PUBLICATIONS

by S.V. Munda

CRC Technical Report No. 90-0

(CSL TN No. 90-364)

February 1990

CENTER FOR RELIABLE COMPUTING
Computer Systems Laboratory
Depts. of Electrical Engineering and Computer Science
Stanford University
Stanford, CA 94305-4055 USA

ABSTRACT

This report is a bibliography of the 1989 CRC Publications.

This report was supported in part by the Innovative Science and Technology Office of the Strategic Defense Initiative Organization and administered through the Office of Naval Research under Contract No. N00014–85–K–0600, and in part by the National Science Foundation under Grant No. MIP-8709128.
TABLE OF CONTENTS

List of Publications
  Journal Papers.................................................................3
  Conference Papers..............................................................4
List of 1989 CRC Technical Reports (TR's).................................5
List of 1989 Presentations...........................................................6
1989 Visitors............................................................................6
1989 Visiting Scholars..............................................................6
List of CRC PhDs Granted..........................................................6
Technical Facilities....................................................................7
LIST OF PUBLICATIONS

JOURNAL PAPERS

Published


Accepted


CONFERENCE PAPERS

Published


Accepted

CRC TECHNICAL REPORTS


PRESENTATIONS

Tutorial at COMPCON Spring '89, Stanford University, Feb. 25, 1989.


1989 VISITORS

Rene Micolet, CEA-IRDI, Leti Division, France, Jan. 1989
Dr. Shanker Singh, IBM, Tucson, AZ. Jan. 1989

David Giramma, Mentor Graphics, Beaverton, OR, Feb. 1989

Kiyoshi Emi, NEC Corporation, Minato-Ku, Tokyo, Japan, Mar. 1989
Xavier Flinois, Schlumberger Technologies, San Jose, CA, Mar. 1989

Johan Karlsson, Chalmers University of Technology, Göteborg, Sweden, Aug. 1989
Ulf Gunneflo, Chalmers University of Technology, Göteborg, Sweden, Aug. 1989

1989 SCHOLARS VISITING CRC

Toyokazu Tatsuta, Hitachi, Ltd. Yokohama, Japan
Katsunobu Muroi, Mitsubishi Electric Corporation, Kanagawa Prefecture 247, Japan
Hideho Yamamura, Hitachi, Ltd., Yokohama, Japan

Tomoo Fukazawa, NTT Laboratories, Kanagawa Prefecture, Japan
Hisashi Yamauchi, NEC Corporation, Kanagawa Prefecture, Japan

CRC PhDs GRANTED

TECHNICAL FACILITIES
CENTER FOR RELIABLE COMPUTING

Apple Macintosh Personal Computers: Four Macintosh personal computers, each with 1M internal memory and two disk drives. An Appletalk network connects the Macs to an Apple Laserwriter printer.

IBM Personal Computers: IBM PC-AT personal computer with enhanced graphics board, high-resolution color monitor, hard disk, and printer. Includes ViewLogic software for schematic capture, simulation, waveform analysis, and fault grading. IBM PC-RT reduced instruction set personal computer with high resolution color monitor, hard disk, streaming tape drive, and Ethernet capability.


Laboratory Oven: AES Model ZCK-9204 laboratory oven to be used for accelerated life testing and burn-in.

Sun 3/140 work station running Unix, with two 64M hard disks, 8M main memory and streaming tape drive. Ethernet capability #708ES613.

Delni-aa ethernet multiplexer and a transceiver providing access to the ethernet for the Microvax, Sun, TEK 4317, and PC-RT. Capability to extend access to four more workstations.

Tektronix Lab Instruments: MHz function generator, 40 MHz function generator - very fancy, 2 Triple power supplies, 2 Digital Multimeters, 5 Coax cables for connecting FGs to boards, 2 Mainframes to hold one each of PS, DM, and FG.

Tektronix Design Analysis System (DAS 9200): Motorola 68010-based test system with 2M memory, 20M hard disk, and color display. Equipped with a 32-channel 50 MHz pattern generation card, two 16-channel 200MHz data acquisition cards, 92 DV device verification software, and TF 100 test fixture.

DAS9200: 1 Tester mainframe, keyboard and monitor, 16-channel 200 MHz data acquisition expander, 16-channel 200 MHz data acquisition expander without probes, 36 Channel 50MHZ sequence pattern generator.

DAS9252: 1 Tester mainframe, keyboard and monitor. Includes board and accompanying flying lead set for microprocessor testing (90 channels), 16-channel 200 MHz data acquisition expander, 16-channel 200 MHz data acquisition expander without probes, 18-channel 50 MHz pattern generator, Test fixture, 21x21 pin grid array fixture to fit TF100

1241BNO-1B: 1 Color logic analyzer mainframe, 2 18-channel cards, 1 9-channel card, 5 P6460 probes, performance analysis ROM pack, 64K RAM pack, parallel printer COMM pack, printer support ROM pack

2467: 1350 MHz four channel portable oscilloscope with word recognizer.

4317: 1 Color graphics workstation running UTek (Tektronix Unix) with 60M and 35M hard drives, and 4M main memory. Ethernet capability.

4696: 1 Color ink-jet printer.
TECHNICAL FACILITIES
SOFTWARE SYSTEMS

The lab houses 7 digital testing Systems: Teradyne, EDA's Lasar, Genrad's HILO, HHB's CADAT and Intelligen, and Gateway's Verilog.

**Lasar**: This VMS-based system is hosted on a MicroVAX and consists of logic and fault simulators, and ATPG for both combinational and sequential circuits. The system includes large model libraries.

**Hilo**: GenRad's Hilo comprises logic and fault simulators and a test pattern generation.

**CADAT 6.1**: CADAT is a logic and fault simulator that is suitable for combinational and sequential circuits. Intelligen is a sequential test pattern generator.

**Verilog**: Hardware description language and simulator.

**Viewlogic**: This is a schematic capture and a logic simulator that is also capable of injecting some faults in the design. The system is mainly used for logic design courses. The version hosted on the Vaxstation also has the capacity to simulate VHDL behavioral models.

**Spice**: Spice systems for analog simulation of devices and circuits: tSpice is hosted on the Tektronix and pSpice on the IBM AT.